

Front End Hardware for Future Generation Wireless Communication System

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AGENDA

- WTEC Study
- Device
- Packaging & Integration
- Amplifiers
- Antennas
- Future Issues & Directions

Background

- State of the Art Hardware at Industries in the US, Europe and Japan
- Improvement vs. Emerging Technologies
- What We Can Learn for the Future

Observation

Research Opportunities Worldwide

- Devices and Materials
 - New Materials and Components (GaN, SiGe, MEMS, etc)
 - Higher f_t & f_{max} , Higher Linearity
 - New 3D-Oriented Process Technology
 - Higher Performance Passive Devices & Antennas

Observation (cont'd)

- Architecture
 - Amplifier Linearization and Efficiency
 - Interconnect & Packaging
 - Mixed Signal IC (Baseband/RF)
 - Front-end Architecture for Software Oriented Radio
 - RF for Smart Antennas
 - Multifunction/Reconfigurable Devices/Circuits/Antennas
 - MEMS

Observation (cont'd)

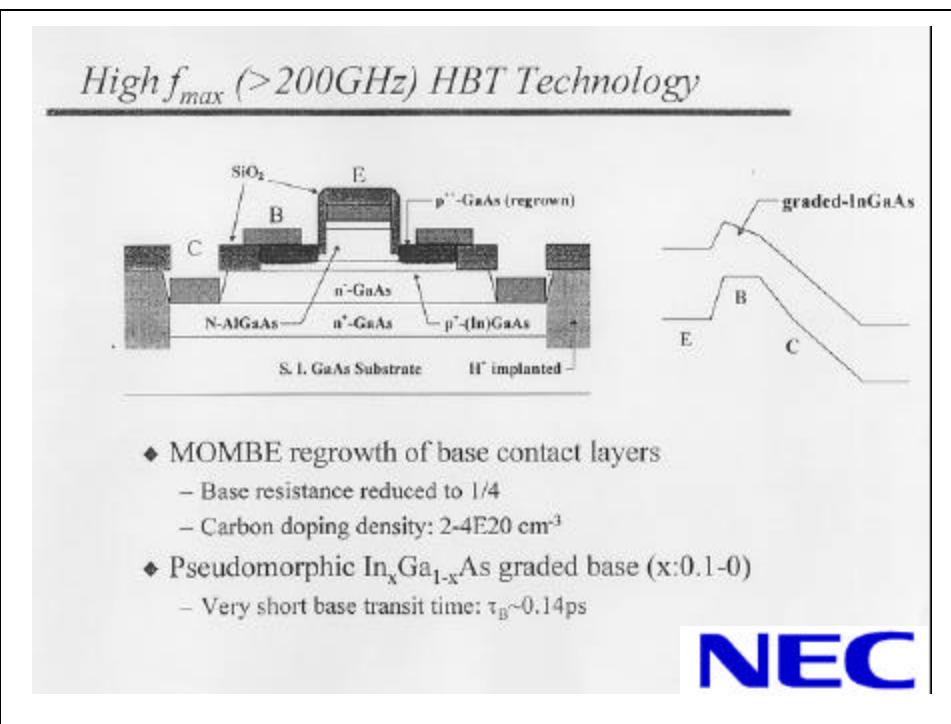
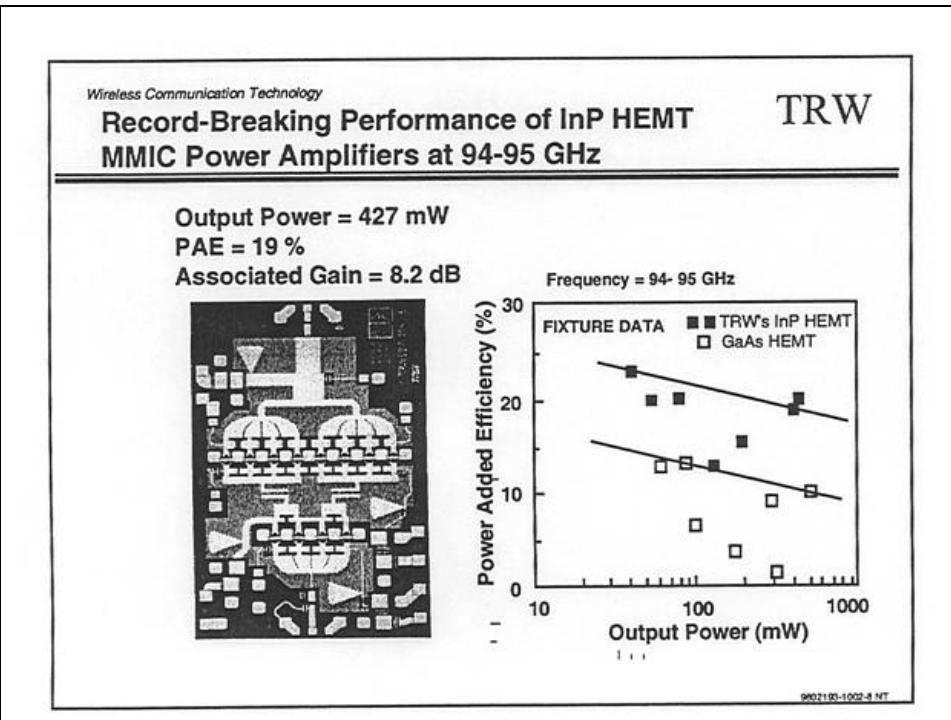
- Frequency
10, 30, 60, 77 and 90
- CAD
Global CAD (Ckt, EM, Device, Ant,
Thermal, Mechanical, Packaging)

DEVICES

Figure of Merit of ADC

$= \log_2(\text{Sampling Speed}) + \text{Resolution}(\text{Effective No. of bits})$
 $\approx 38 \sim 40$ (*State of the art*)

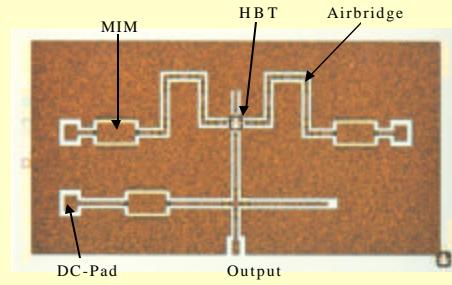
e.g. $\log_2(1 \text{ GHz}) + 10 \text{ bits} = 40$



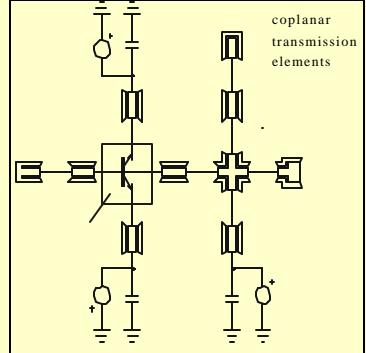
SiGe SiMMWIC: Modules

Ka-Band Moduls for Minilinks

Layout of coplanar Oscillators



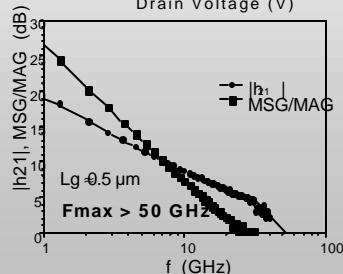
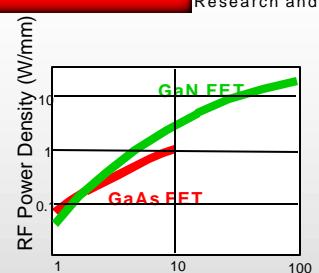
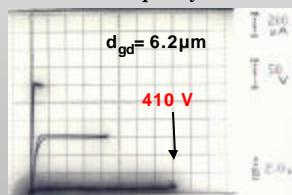
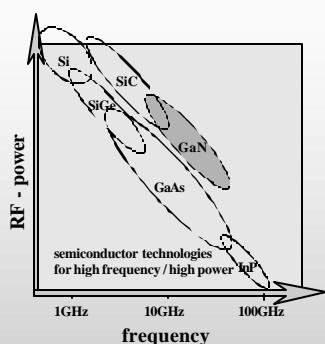
Circuit diagram
coplanar oscillator



DaimlerChrysler

GaN - Power Technology

Research and Technology



DaimlerChrysler

Luy, Lab 13, Folie 12

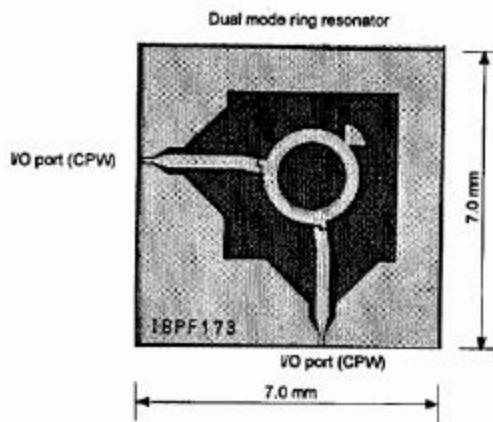
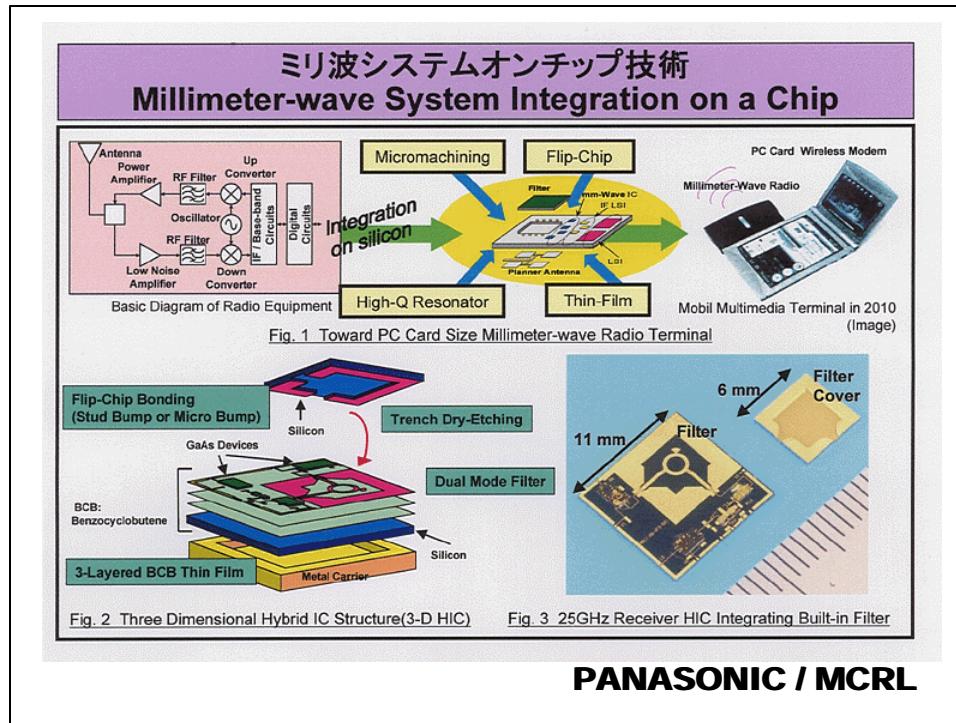
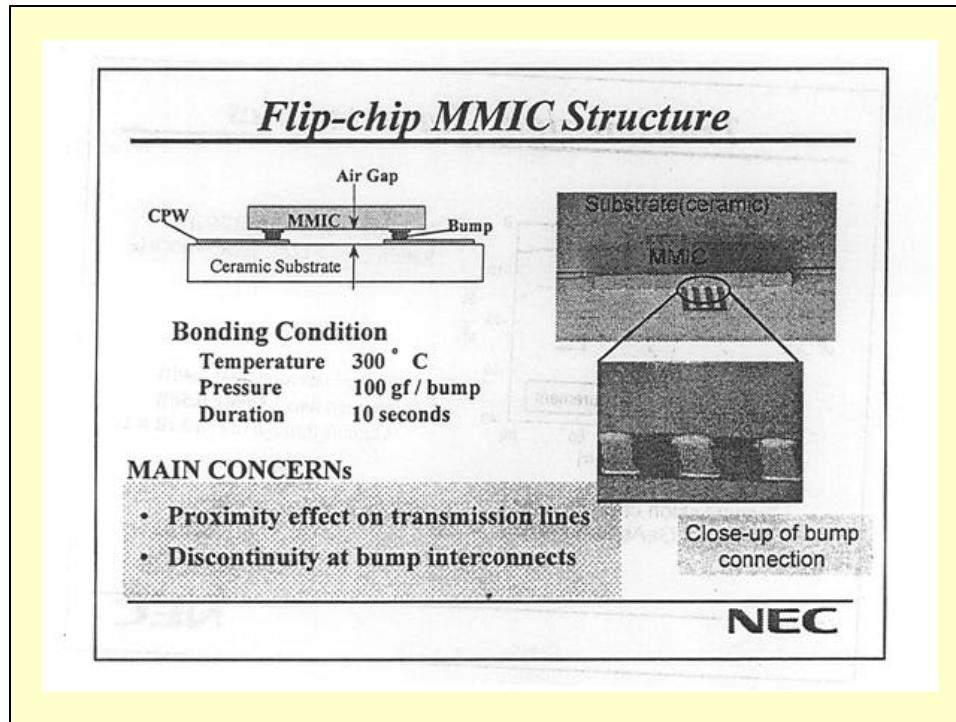


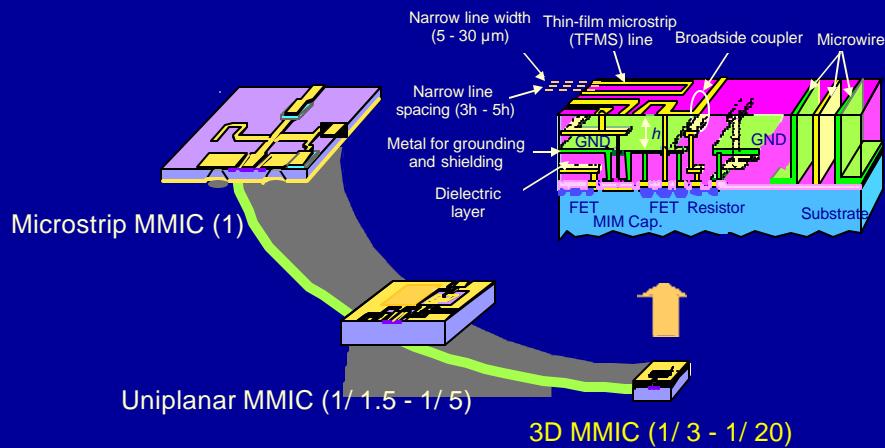
Figure 4 Microphoto of the experimental dual mode filter
on the silicon substrate
(without micromachined silicon cover)

Packaging and Integration



Miniaturization of MMIC

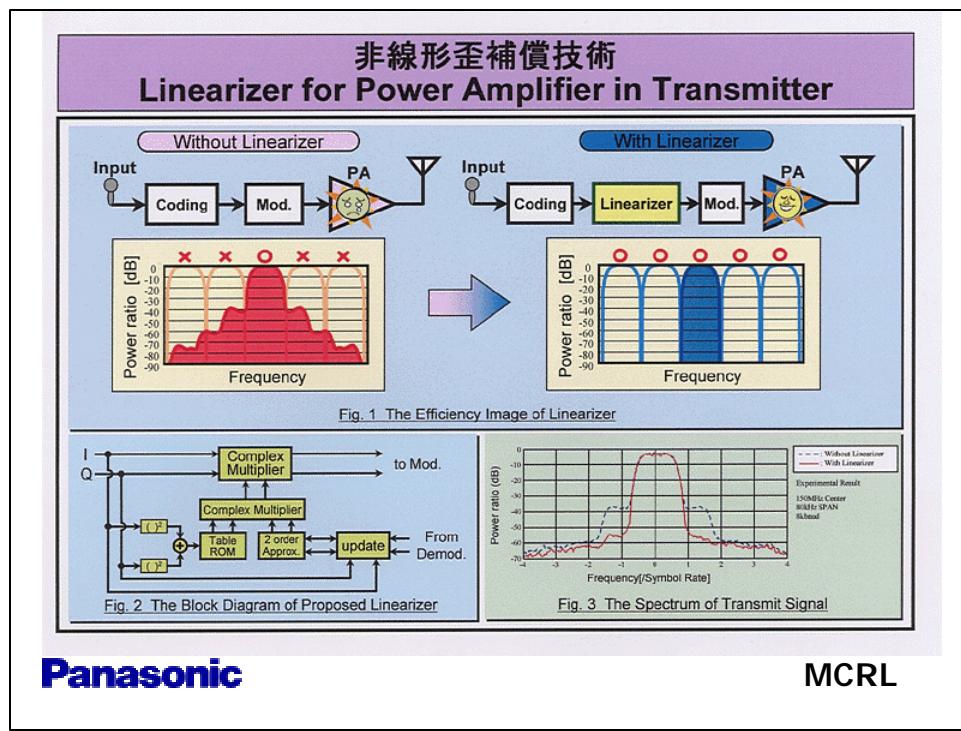
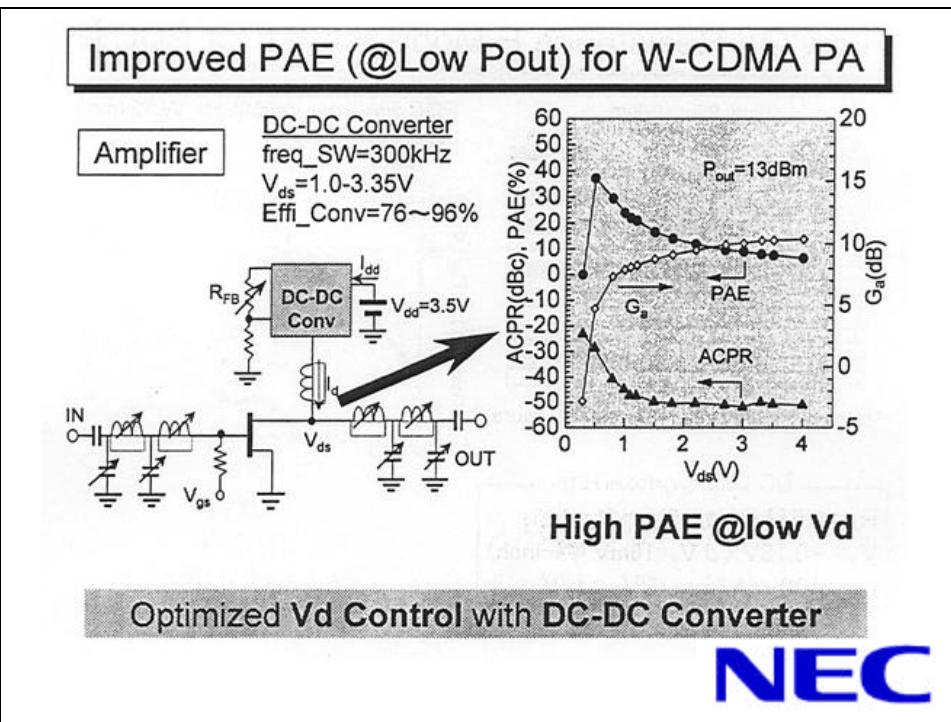
NTT ©



3D MMIC Technology

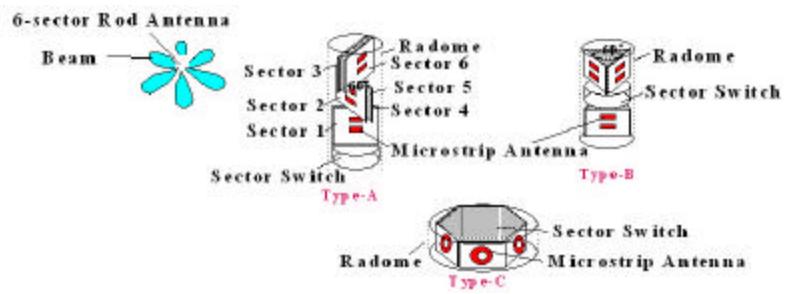
Wireless Systems Laboratory

Amplifiers



Antennas

Rod Type Printed Antennas



 NTT

FUTURE ISSUES & DIRECTIONS

Amplifier Efficiency and Linearity

- Synergistic approach
- Good devices are needed (SiGe HBT?)
 - By architecture as front-end
 - In combination with digital technique

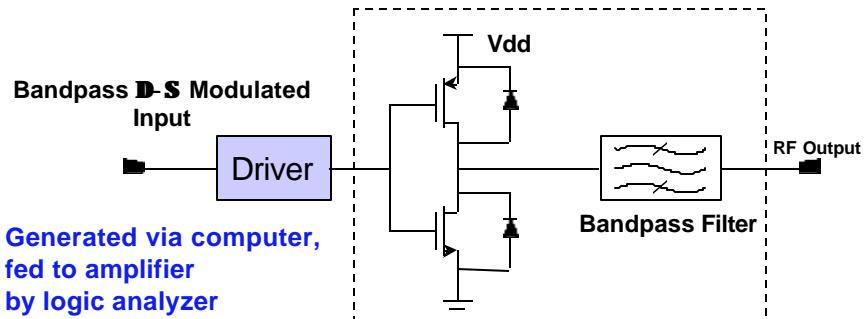
Mixed Signal IC

- Toward One-chip Radio or System on a Chip
- Good devices are needed (SiGe HBT?)



Class S Amplifier for Bandpass D-S Modulator

Preliminary Implementation: Complementary MOSFETs
(low frequency demonstration: 10MHz)

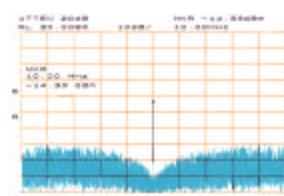


Class S Amplifier



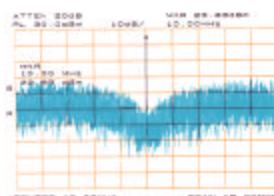
Class S Amplifier for Bandpass D-S Modulator

Bandpass D-S
Modulated Input (50W)



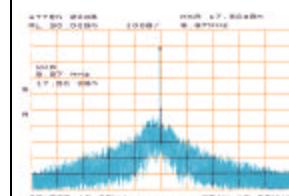
Input

Class S Amplifier
Output (50W)

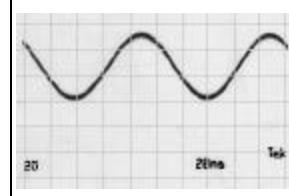
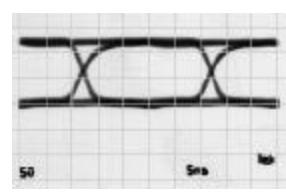
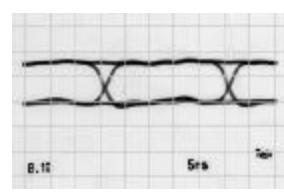


Amplifier

RF Output (50W)



Output





Applications of DSP in Power Amplifiers

- * Generate input signals at baseband
- ? Generate reference signals (eg: envelope)
- ? Generate control signals for PA (eg: V_{gg} control)
- ? Predistort input signal (at baseband)
- ? Generate waveforms for PWM converter
- ? Generate rf waveforms

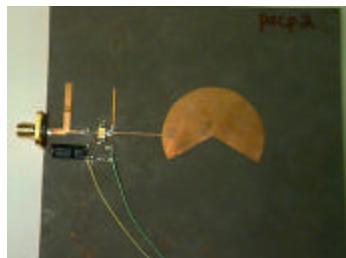
Instead of “High Efficiency and Linear Power Amplifier”

====> Focus Becomes “High Efficiency and Linear Transmitter”

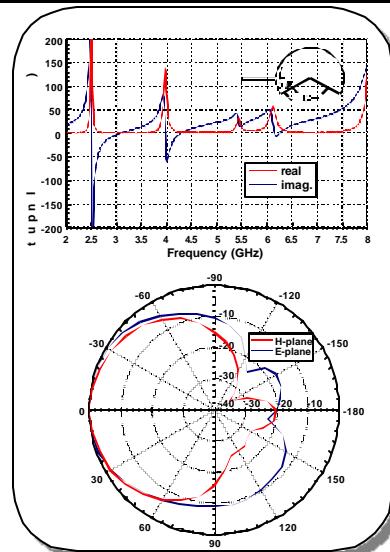
Need to integrate PA design into overall transmitter design



Class F Amplifier Integrated with Antenna



- Class F power amplifier requires tuning of 2nd and 3rd harmonics
 - Used circular segment microstrip antenna
- Real part of input impedance is almost zero at 2nd and 3rd harmonics
- Measured Antenna Gain = 5.8 dB
- Cross-Polarization < -16 dB



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Front End Architecture for Software Oriented Radio

- Hardware aspects should not take a back seat in research

RF for Smart Antenna

- Need antenna with smart function electronics

Multifunction/Reconfigurable Devices/Circuits/Antennas

- Even if everything else becomes digital, antenna will not be.

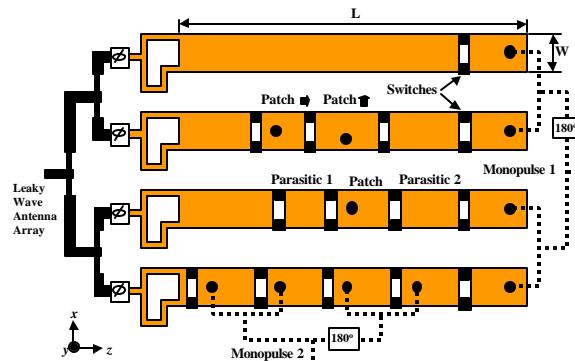


Reconfigurable Leaky Mode/Multifunction Patch Aperture

Leaky-Mode Antennas & Arrays Find Wide-Range Applications in Radars and Wireless Communications

- Relatively high gain
- Easy Realization of Fan Beam
- Frequency Scanning for Beam-Steering Arrays

Proposed UCLA Structure



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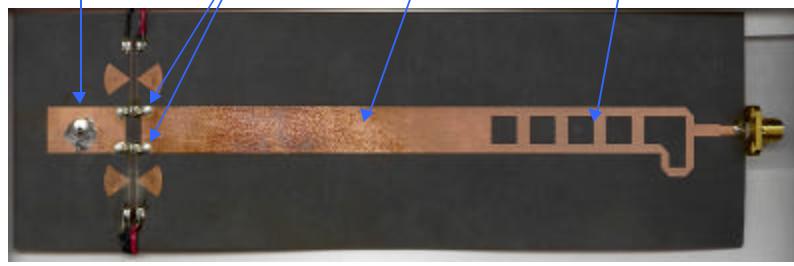
Reconfigurable Antennas

Patch antenna
aperture and
feed

PIN diode
switches

Leaky mode
antenna
aperture

First higher
order mode
launcher

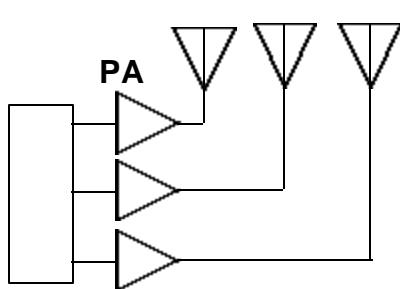


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Another Future Challenge

Phase distortion must be minimized
for proper operation of “smart antennas”



*AM/PM distortion
of PAs will produce
pointing error,
sidelobes,
and incomplete nulls*

Can be more stringent
than specifications
on ACPR

Technology Assessment

	US	Japan	Europe
Millimeter Wave	2	5	4
Packaging/Interconnect	5	4	3
CAD	5	2	3
SiGE	4	3	5*
III-V	5	5	4
GaN	5	4	3
Antennas	4	4	4
Passive Components	5	5	4
Amplifier Technique	5	4	4**
MEMS	4	3	2

*Germany Only

** UK